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SUN MICROSYSTEMS, INC., ("assignee"), a Delaware corporation having a place of business at 4150 Network Circle, Santa Clara, California 95054, certifies that to the best of assignee's knowledge and belief it is the assignee of the entire right, title, and interest in and to the above-referenced patent and represents that the undersigned is a representative authorized and empowered to sign on behalf of the assignee.

Assignee has reviewed the assignment documents that evidence the placement of title in the assignee, true and correct copies of which are attached hereto, and understands and believes that these assignment documents have been submitted for recordation in the U.S. Patent and Trademark Office.

Pursuant to 37 C.F.R. §§1.36 and 3.71, the assignee hereby revokes all powers of attorney previously given and appoints the following attorneys and/or agents in connection with the patent applications and patents identified in Appendix A, owned by SUN MICROSYSTEMS, INC., that are filed with the United States Patent and Trademark Office:

Andrew C. Chen	Reg. No. 43,544	Noreen A. Krall	Reg. No. 39,734
Bernice B. Chen	Reg. No. 42,403	Elaine K. Lee	Reg. No. 41,936
Arien C. Ferrell	Reg. No. 46,696	Sean P. Lewis	Reg. No. 42,798
Marilyn E. Glaubensklec	Reg. No. 35,521	Jeffrey L. Myers	Reg. No. 44,252
Anirima R. Gupta	Reg. No. 38,275	Joseph A. Twarowski	Reg. No. 42,191

each of said attorneys being employed by Sun Microsystems; and

Mark K. Brightwell	Reg. No. 47,446	Lawrence J. Merkel	Reg. No. 41,191
Kay A. Colapret	Reg. No. 52,759	Eric B. Meyertons	Reg. No. 34,876
Stephen J. Curran	Reg. No. 50,664	Neal E. Persky	Reg. No. 53,452
Mark R. DeLuca	Reg. No. 44,649	Liza Philip	Reg. No. 51,352
Heather L. Flanagan	Reg. No. 54,101	David W. Quimby	Reg. No. 39,338
Russell Henrichs	Reg. No. 50,354	Rory D. Rankin	Reg. No. 47,884
Erik A. Heter	Reg. No. 50,652	Gareth Sampson	Reg. No. 52,191
Jeffrey C. Hood	Reg. No. 35,198	Chris Thompson	Reg. No. 43,188
B. Noël Kivlin	Reg. No. 33,929	Mark S. Williams	Reg. No. 50,658
Robert C. Kowert	Reg. No. 39,255		

each of said attorneys or agents being a member or an associate of the firm of Meyertons, Hood, Kivlin, Kowert & Goetzl, P.C., as its attorney or agent for so long as they remain with such firm, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive any Letters Patent, and for one year after issuance of such Letters Patent to file any request for a certificate of correction that may be deemed appropriate.

Pursuant to 37 C.F.R. §3.71, the assignee hereby states that prosecution of the above-referenced patent application is to be conducted to the exclusion of the inventor(s).

Send all future correspondence to:

B. Noël Kivlin
Meyertons, Hood, Kivlin, Kowert & Goetzl, P.C.
P.O. Box 398
Austin, Texas 78767-0398
(512) 853-8800

Assignee of Interest

SUN MICROSYSTEMS, INC.
4150 Network Circle
Santa Clara, California 95054

Dated: June 23, 2004

By: Noreen A. Krall
Noreen A. Krall, Reg. No.: 39,734
Chief Patent Counsel



MIKKG Ref	Sub Ref	Issued/ Filed Date	Patent/ Serial#	Title	Inventor(s)
6000-03100	P1984	I 1/11/00 F 12/9/96	6,014,660 08/763,234	Method and Apparatus for Client-Sensitive Name Resolution Using DNS	Swee B Lim, Sanjay R Radia, Thomas K Wong
6000-03200	P2015	I 8/17/99 F 12/9/96	5,938,732 08/763,289	Load Balancing and Failover of Network Services	Swee B Lim, Ashish Singhai, Sanjay R Radia
6000-03300	P2016	I 3/16/99 F 12/9/96	5,884,024 763,068	Secure DHCP Server	Swee B Lim, Sanjay R Radia, Thomas K Wong, Panagiotis Tsirigotis, Robert J Goedman
6000-03400	P2018	I 5/30/00 F 12/9/96	6,070,242 08/763,212	Method to Activate Unregistered Systems in a Distributed Multiserver Network Environment	Thomas K Wong, Sanjay R Radia
6000-03500	P2020	F 8/11/97	08/909,369	Method and Apparatus for Assignment of IP Addresses	Thomas K Wong, Swee B Lim, Sanjay R Radia, Panagiotis Tsirigotis, Robert J Goedman, Michael W Patrick
6000-03501	P2020C	I 6/6/00 F 12/9/96	6,073,178 08/762,709	Method and Apparatus for Assignment of IP Addresses	Thomas K Wong, Swee B Lim, Sanjay R Radia, Panagiotis Tsirigotis, Robert J Goedman, Michael W Patrick
6000-03600	P2027	I 7/13/99 F 12/9/96	5,922,049 08/762,933	Method for Using DHCP and Marking to Override Learned IP Addresses in a Network	Sanjay R Radia, Thomas K Wong, Swee B Lim, Panagiotis Tsirigotis, Robert J Goedman, Michael W Patrick
6000-03700	P2063	I 3/12/02 F 9/18/98	6,356,637 09/157,205	Field Programmable Gate Arrays	Paul Jeffrey Garnett
6000-03800	P2211	I 7/10/01 F 7/17/98	6,260,138 09/118,642	Method and Apparatus for Branch Instruction Processing in a Processor	Jeremy G Harris
6000-03900	P2462	I 9/19/00 F 12/31/98	6,122,745 09/224,437	Method and Apparatus for Managing Power Consumption in Data Processing Systems	Becky H Wong-Insley
6000-04000	P3153	I 4/1/03 F 10/4/99	6,542,900 09/412,096	Method and Apparatus for Distributed Object Mapping in a Java Language Environment	Chun R Xia
6000-04100	P3154	I 11/28/00 F 6/30/98	6,154,849 09/107,229	Method and Apparatus for Resource Dependency Relaxations	Chun R Xia
6000-04200	P3196	I 5/6/03 F 10/30/98	6,560,629 09/183,760	Multi-Thread Processing	Jeremy G Harris
6000-04300	P3321	I 4/2/02 F 11/3/98	6,367,029 09/185,414	File Server System Tolerant to Software and Hardware Failures	Martin Mayhead, Graham Parrington, James Radley, Gradimir Starovic
6000-04400	P3329	I 8/7/01 F 11/17/98	6,272,522 09/193,645	Computer Data Packet Switching and Load Balancing System Using a General-Purpose Multiprocessor Architecture	Cher-Wen Lin, Kumar Ramaswamy, Mizanur Mohammed Rahman, Randall David Rettberg, Robert Arthur Doolittle
6000-04500	P3330	I 6,272,136 F 11/16/98	6,272,136 09/193,205	Pseudo-Interface Between Control and Switching Modules of a Data Packet Switching and Load Balancing System	Cher-Wen Lin, Kumar Ramaswamy, Mizanur Mohammed Rahman, Randall David Rettberg



MIKING Ref	Sum Ref	Issued/ Filed Date	Patent/ Serial #	Title	Inventor(s)
6000-04600	P3333	I 6,424,621 F 11/17/98	6,424,621 09/193,652	Software Interface Between Switching Module and Operating System of a Data Packet Switching and Load Balancing System	Kumar Ramaswamy, Cher- Wen Lin, Randall David Rettberg, Mizanur Mohammed Rahman
6000-04700	P3334	I 11/21/03 F 11/16/98	6,510,164 09/193,651	User-Level Dedicated Interface for IP Applications in a Data Packet Switching and Load Balancing System	Kunar Ramaswamy, Cher- Wen Lin, Randall David Rettberg, Mizanur Mohammed Rahman
6000-04900	P3524	I 6/15/04 F 4/18/00	6,751,655 09/551,522	Method and Apparatus for Transport of Scenograph Information Across a Network	Keith Deutsch, Pallavi Shah, Gerard Fernando
6000-05000	P3525	F 10/17/00	09/691,407	Method and Apparatus for Organizing Elements of a Server Application in a Client-Server System	Keith Deutsch, Pallavi Shah, Gerard Fernando
6000-05100	P3526	F 10/20/00	09/693,355	Method and Apparatus for Creating and Deploying Applications from a Server Application	Pallavi Shah, Keith Deutsch, Gerard Fernando
6000-05200	P4385	I 5/18/04 F 5/12/00	6,738,915 09/569,535	System for Supplying Multiple Voltages to Devices on Circuit Board Through a Sequencing in a Predictable Sequence	James A Mott, William M Baldwin
6000-05300	P4554	I 1/7/03 F 6/27/00	6,504,728 1/7/03	Protective Device for Installing CPCI Microprocessor Boards	Peter Cuong Dac Ta
6000-05400	P4559	F 10/23/02	10/278,697	Accessing Rendered Graphics Over the Internet	Satyanarayana Nishtala, Peter Denyer
6000-05500	P5040	I 9/9/03 F 12/19/00	6,617,841 09/741,123	Method and Apparatus for Characterization of Electronic Circuitry	Hemavann Thao
6000-05600	P5338	I 9/17/02 F 2/13/01	D462,968 29/137,146	Bezel for Computer Workstation	Milton C Lee
6000-05700	P5389	I 4/2/02 F 2/13/01	D455,148 29/137,148	Data Storage Device	Milton C Lee, James M Stanton
6000-05800	P5434PSP	F 8/9/02	60/413,015	Common Driver for Multiple Controllers	Shiv Kumar Rajpal, Tanjore Shuresh
6000-05801	P5434	F 9/25/02	10/254,953	Common Driver for Multiple Controllers	Shiv Kumar Rajpal, Tanjore Shuresh
6000-05900	P5461	I 11/11/03 F 6/4/01	6,646,868 09/873,825	Computer Bus Rack Having an Increased Density of Card Slots	Raymond H Ho, Victor E Jochiong, Richard R Creason
6000-06000	P5462	F 9/12/01	09/953,503	Front Access Only CPCI Computer System	Raymond K Ho, Richard R Creason, Kaamel M Kermaani
6000-06100	P5463	I 7/9/02 F 4/2/01	6,418,026 09/824,467	Bus Rack for Accommodating Plural Stand- Alone Computers	Raymond K Ho, Richard R Creason, Victor E Jochiong
6000-06200	P5464	I 12/31/02 F 6/22/01	6,501,660 09/887,807	Reliable Card Detection in a CPCI System	Raymond K Ho, Victor E Jochiong
6000-06300	P5465	F 7/26/01	09/916,882	Computer System Having a Hot Swappable Hot Swap Controller	Raymond K Ho, Victor E Jochiong
6000-06400	P5466	I 1/13/04 F 10/23/01	6,677,687 10/000,553	System for Distributing Power in CPCI Computer Architecture	Raymond K Ho, Richard R Creason, Victor E Jochiong



MIKING Ref	Sum Ref	Issued/ Filed Date	Patent/ Serial #	Title	Inventor(s)
6000-06500	P5603PSP	F 8/9/02	60/413,016	Generic Interface and Framework to Publish and Monitor Platform Configuration	Srinivasan Subramanian, John G Johnson, Gregory C Onufer, Richard A Zatorski
6000-06501	P5603	F 9/25/02	10/254,952	Generic Interface and Framework to Publish and Monitor Platform Configuration	Srinivasan Subramanian, John G Johnson, Gregory C Onufer, Richard A Zatorski
6000-06600	P5645	F 2/16/01	09/785,927	Integrated Driver Electronic (IDE) Device Power Control	Rong-Dyi Chen
6000-06700	P6336	F 3/29/02	10/109,761	High Air Flow Fan Tray Bracket	Vince P Hileman, Robert J Lajara, Thomas E Stewart
6000-06800	P6342	I 6/24/03 F 4/12/02	6,583,991 10/121,494	High Air Flow Inlet Bezel	Steven J Furuta, Robert J Lajara, Timothy E Mautz, Thomas E Stewart
6000-06900	P6344	I 1/20/04 F 8/6/01	6,679,721 09/924,223	Module Insertion Tool	Dong-Hyon Kim
6000-07000	P6784	F 1/23/02	10/055,692	Auto-SCSI Termination Enable in a CPCI Hot Swap System	Maire Mahony, Gerald R. Pelissier
6000-07100	P6920	F 9/27/02	10/259,919	Exterior Front Panel Assembly for Accommodating and Strengthening a Computer Card Assembly	Peter Cuong Dac Ta
6000-07200	P7367	I 2/17/04 F 4/22/03	D486,832 29/180,331	Computer Monitor Assembly	Kuni Masuda, James Stanton, Christopher H. Frank
6000-07300	P7403	F 4/14/03	10/413,281	Snap-Together Modular Fan Tray Assembly for an Electronics Enclosure	Thomas E Stewart, Robert J. Lajara
6000-07400	P7553	I 4/13/04 F 7/29/02	6,720,774 10/208,162	Interchangeable Fan Control Board with Fault Detection	Carl L Meert, Thomas E Stewart, Timothy W Olesiewicz
6000-07500	P7567	I 3/23/04 F 5/30/02	6,708,372 10/160,927	Snap-in Fan Tray Ejector and Handle	Thomas E Stewart
6000-07600	P7568	I 4/15/03 F 5/9/02	6,549,406 10/142,100	Fan Tray Assembly for an Electronics Enclosure	Timothy W Olesiewicz, Robert J Lajara
6000-07700	P7647	F 6/12/03	10/459,882	System and Method for Providing Switch Redundancy Between Two Server Systems	Balkar S Sidhu, Ramani Krishnamurthy, Kaamel Kermaani
6000-07800	P7749	F 12/11/02	10/317,520	System and Method for Interconnecting Nodes of a Redundant Computer System	Balkar S Sidhu, Ramani Krishnamurthy
6000-07900	P7757	I 3/30/04 F 3/11/02	6,713,678 10/096,349	Cable Management System for Electronic Devices Such as Flat Panel Monitors	Kuni Masuda, Joe Miseli
6000-07901	P7757DIV	I 10/28/03 F 10/1/02	6,637,104 10/263,050	Cable Management System for Electronic Devices Such as Flat Panel Monitors	Kuni Masuda, Joe Miseli
6000-08000	P7758	F 3/11/02	10/096,351	Hideable Accessory Platform	Kuni Masuda, Joe Miseli
6000-08100	P7759	I 6/10/03 F 3/12/02	6,575,419 10/097,240	Universal Support System for Displays	Kuni Masuda, Joe Miseli, James Stanton
6000-08200	P7760	I 5/13/03 F 3/12/02	6,561,469 10/096,778	High Storage Volume Support for Displays	Kuni Masuda, Joe Miseli, James Stanton
6000-08300	P7761	F 3/12/02	10/097,060	Stable Support System for Displays	Kuni Masuda, Joe Miseli, James Stanton



MIHKG Ref	Sub Ref	Issued/ Filed Date	Patent/ Serial #	Title	Inventor(s)
6000-08400	P7762	F 3/12/02	10/097,239	Multi-Hinge System for Displays	Kuni Masuda, Joe Miseli
6000-08500	P7763	F 3/11/02	10/096,348	Method and Apparatus for Automatically Presenting Information on a Display	Kuni Masuda, Joe Miseli
6000-08600	P7784	I 4/27/04 F 3/11/02	6,727,824 10/095,667	Method and Apparatus for Thermal Management of a Display	Joe Miseli, Tom Fussy, Clayton Castle, John Rahn
6000-08700	P7877	F 4/8/02	10/119,929	Staggered Lamp Inverter Dimming Method and Apparatus for Displays	Joe Miseli
6000-08800	P7904	F 6/6/03	10/455,938	Methods and Apparatus for Configuring a Packet Switching (PS) Backplane to Support Various Configurations	Ramani Krishnamurthy, Raymond Ho, Balkar S Sidhu
6000-08900	P7920	F 10/10/02	10/268,387	System and Method for Expanding the Management Redundancy of Computer Systems	Kaamel M. Kermaani, Ramani Krishnamurthy, Balkar S Sidhu
6000-09000	P7952	F 10/10/02	10/268,325	Apparatus and Methods for Redundant Management of Computer Systems	Ramani Krishnamurthy, Raymond Ho, Viswanath Krishnamurthy
6000-09100	P7975	F 1/23/03	10/351,618	Methods and Apparatus for Inspecting Centerplane Connectors	John F Kennedy, Chuck V Ho, John S Mew, Ricky A Smith, Thomas E Roach, Brian T Evans, Greg I Henry
6000-09200	P7992	F 5/8/03	10/435,733	Supporting Non-Hotswap 64-bit CPCI Cards in a HA System	Ramani Krishnamurthy, Srinivas Susarla
6000-09300	P8040	F 12/11/02	10/317,538	Methods and Apparatus for Providing Microprocessor Firmware Control of Power Sequencing on a CPCI Card	Maire Mahony, Gerald Pelissier
9400	P8046	F 9/18/02	10/247,182	Heat Sink and Electromagnetic Interference Reduction Device	Thomas E Stewart, Steven J. Furuta
6000-09500	P8524	F 2/18/03	10/370,413	Extending Non-Volatile Memory Endurance Using Data Encoding	Steven T Sprouse
6000-09600	P8677	F 10/21/02	10/277,338	Protection Element for an EMI Reduction Element	Clifford B Willis, Vincent P Hileman, Rob J Lajara
6000-09700	P8787PSP	F 10/24/02	60/420,924	System Drawer Guides	Clifford B Willis, Rob J Lajara
6000-09701	P8787	F 8/11/03	10/639,175	Computer Chassis Drawer Guide	Clifford B Willis, Rob J Lajara
6000-09800	P8859PSP	F 10/24/02	60/420,849	Virtual Communication Interfaces IPMI	Ranen Chatterjee, Srinivasa Babu Venkatachalam
6000-09801	P8859	F 10/21/03	10/691,071	Virtual Communication Interfaces for a Micro-Controller	Ranen Chatterjee, Srinivasa Babu Venkatachalam
6000-09900	P8941	F 10/24/03	10/693,029	Exporting I2C Controller Interfaces for I2C Slave Devices Using IPMI Micro-Controller	Ranen Chatterjee
6000-10000	P8952PSP	F 10/24/02	60/420,925	DHCP Client-ID Generation	Viswanath Krishnamurthy, Mir J Hyder, Sunit Jain
6000-10001	P8952	F 10/23/03	10/693,583	System and Method for DHCP Client-ID Generation	Viswanath Krishnamurthy, Mir J Hyder, Sunit Jain



MIKKG Ref	Stm Ref	Issued/ Filed Date	Patent/ Serial #	Title	Inventor(s)
6000-10100	P8953PSP	F 10/24/02	60/420,923	Persistent Power Mask	Viswanath Krishnamurthy, Daniel Delfatti
6000-10101	P8953	F 10/23/03	10/693,710	System and Method for Providing a Persistent Power Mask	Viswanath Krishnamurthy, Daniel Delfatti
6000-10200	P8954PSP	F 10/24/02	60/420,850	Mapping of Hot Swap States of PICMG 2.1 to TMN Plug-in Units	Tuan Le, Angshuman Mukherjee, Christopher Rinaldo, Vinh Truong, Daniel Delfatti
6000-10201	P8954	F 10/20/03	10/690,121	Mapping of Hot-Swap States to Plug-in Unit States	Tuan Le, Angshuman Mukherjee, Christopher Rinaldo, Vinh Truong, Daniel Delfatti
6000-10300	P8964	F 8/26/03	10/648,528	Management Methods and Apparatus that are Independent of Operating Systems	Tuan A Le, Christopher J Rinaldo, Angshuman Mukherjee, Vinh N Truong, Daniel Delfatti
6000-10400	P8965PSP	F 10/24/02	60/420,931	Attachment for a Rotating Part	Clifford B Willis, Robert J Lahara
6000-10401	P8965	F 8/11/03	10/639,334	Attachment for a Rotating Part	Clifford B Willis, Robert J Lahara
6000-10500	SUN030174	F 11/10/03	10/704,939	Portable Thin Client for the Enterprise Workspace	James E King, Karen E Roles



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JANUARY 03, 2003

O'MELVENY & MYERS LLP
BRIAN M. BERLINER
400 SOUTH HOPE STREET
LOS ANGELES, CA 90071-2899

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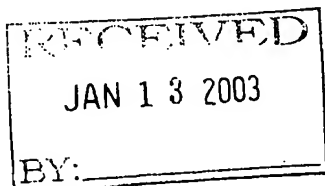
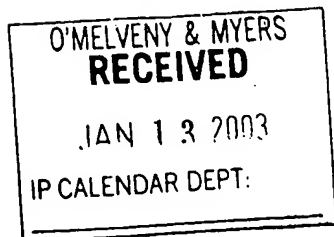
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PELISSIER, GERALD R.

DOC DATE: 01/15/2002

ASSIGNEE:
SUN MICROSYSTEMS, INC.
901 SAN ANTONIO ROAD
MS PAL01-521
PALO ALTO, CALIFORNIA 94303

SERIAL NUMBER: 10055692
PATENT NUMBER:

FILING DATE: 01/23/2002
ISSUE DATE:



O'Melveny & Myers LLP

File: 843161-106

Action Item: —

Date Due: —

Critical Date: —

Attorney Path: BMB

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MAIRE MAHONY
GERALD R. PELISSIER

Additional name(s) of conveying party(ies) attached? ☐ yes ☒ no

2. Name and address of receiving party(ies):

(2a.)

Name: Sun Microsystems, Inc.

Address: 901 San Antonio Road, MS PAL01-521, Palo Alto, CA 94303

3. Name of Conveyance:

- ☐ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name

☒ Other CORRECTION OF ASSIGNOR'S NAME -

GERALD R. PELISSIER found on REEL /FRAME 012997/0357

Execution Date: January 15, 2002

(2b.)

Name

Address:

Additional name(s) & address(es) attached? ☐ yes ☒ no

4. Application number(s) or patent number(s): 10/055,692

If this document is being filed together with a new application, the execution date of the application is: January 15, 2002

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached ☐ yes ☒ no

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Brian M. Berliner

Internal Address: O'MELVENY & MYERS LLP

Street Address: 400 South Hope Street

City: Los Angeles State: CA ZIP: 90071-2899

6. Total number of applications and patents involved: one

7. Total fee (37 CFR 3.41):.....\$ 40.00

☐ Enclosed

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GERALD R. PELLISSIERAdditional name(s) of conveying party(ies) attached? ☐ yes ☒ no

2. Name and address of receiving party(ies):

(2a.)

Name: Sun Microsystems, Inc.Address: 901 San Antonio Road, MS PAL01-521, Palo Alto, CA 94303

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- ☒ Assignment ☐ Merger
☐ Security Agreement ☐ Change of Name
☐ Other

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Additional name(s) & address(es) attached? ☐ yes ☒ noExecution Date: January 15, 2002

4. Application number(s) or patent number(s):

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B. Patent No.(s)

Additional numbers attached ☐ yes ☒ no

5. Name and address of party to whom correspondence concerning document should be mailed:

Name: Brian M. BerlinerInternal Address: O'MELVENY & MYERS LLPStreet Address: 400 South Hope StreetCity: Los Angeles State: CA ZIP: 90071-28996. Total number of applications and patents involved: one7. Total fee (37 CFR 3.41):.....\$ 40.00

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(1) Maire Mahony (5) _____
 (1-8) Insert Name(s) of Inventor(s) (2) Gerald R. Pelissier (6) _____
 (3) _____ (7) _____
 (4) _____ (8) _____

For good and valuable consideration receipt of which is hereby acknowledged, the undersigned agree(s) to assign, and hereby do(es) assign, transfer and set over to:

(9) Insert name of Assignee (9) Sun Microsystems, Inc.
 (10) Insert state of incorporation of Assignee (10) Delaware
 (11) Insert address of Assignee (11) of 901 San Antonio Road, MS PAL01-521, Palo Alto, CA 94303
 (hereinafter designated as the Assignee) the entire worldwide right, title and interest in the invention known as
 (12) Insert identification of invention, such as Title, Case Number or Foreign Application Number (12) Auto-SCSI Termination Enable In A CPCI Hot Swap System
 (Case No.) (Serial No.) for which the undersigned has (have) executed an application for patent in United States of America
 (13) Insert Date of Signing of Application (13) on JANUARY 15 - 2002

1) The undersigned agree(s) to execute all papers necessary in connection with the application and any continuing or divisional applications thereof and also to execute separate assignments in connection with such applications as the Assignee may deem necessary or expedient.

2) The undersigned agree(s) to execute all papers necessary in connection with any interference which may be declared concerning this application or continuation or division thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference.

3) The undersigned agree(s) to execute all papers and documents and perform any act which may be necessary in connection with claims or provisions of the International Convention for Protection of Industrial Property or similar agreements.

4) The undersigned agree(s) to perform all affirmative acts which may be necessary to obtain a grant of a valid United States patent to the Assignee.

5) The undersigned hereby authorize(s) and request(s) the Commissioner of Patents and the duly constituted authorities of foreign countries to issue any and all Letters Patents resulting from said application or any division or divisions or continuing or reissue applications thereof to the said Assignee, its successors and assigns, as Assignee of the entire right, title and interest, and hereby covenants that he has (they have) full right to convey the entire interest herein assigned, and that he has (they have) not executed and will not execute, any agreement in conflict herewith.

6) The undersigned hereby grant(s)

Brian M. Berliner Reg. No. 34,549; Todd E. Fitzsimmons, Reg. No. 44,683; Dennis R. Gallagher, Reg. No. 42,563; Jonathan A. Jaech, Reg. No. 41,091; Ryan K. Yagura, Reg. No. 47,191; and Peter C. Hsueh, of O'MELVENY & MYERS LLP, 400 S. Hope Street, Los Angeles, California 90071-2899; and Kenneth Olsen, Reg. No. 26,493; Timothy J. Crean, Reg. No. 37,116; Alexander E. Silverman, Reg. No. 37,940; Aniruma Rakshpal Gupta, Reg. No. 38,275; Sean P. Lewis, Reg. No. 42,798; Michael J. Schallap, Reg. No. 44,319; Noreen A. Krall, Reg. No. 39,734; Bernice B. Chen, Reg. No. 42,403; Richard J. Lutton, Jr., Reg. No. 39,756; Marc D. Foodman, Reg. No. 34,110; Monica D. Lee, Reg. No. 40,696; Pavel Pogodin, Reg. No. P48,205; Naren Chaganu, Reg. No. 44,602; Elaine K. Lee, Reg. No. 41,936; Hugh H. Matsubayashi, Reg. No. 43,779; Paul D. Sorken, Reg. No. 39,039; Marilyn E. Glaubensklea, Reg. No. 35,521; Ramlin Aghevli, Reg. No. 43,462; Andrew C. Chen, Reg. No. 43,544 and Jeffrey L. Myers, Reg. No. 44,252, of SUN MICROSYSTEMS, INC., 901 San Antonio Road (MS/PAL01-521), Palo Alto, California 94303

the power to insert on this assignment any further identification which may be necessary or desirable in order to comply with the rules of the United States Patent and Trademark Office for recordation of this document.

Date JANUARY 15 - 2002 Name of Inventor(s) GERALD PELISSIER
 Date _____ Name of Inventor(s) _____
 Date _____ Name of Inventor(s) _____
 Date _____ Name of Inventor(s) _____
 Date _____ Name of Inventor(s) _____

(1) Maire Mahony (5) _____
 (1-8) Insert Name(s) of Inventor(s) (2) Gerald R. Pelissier (6) _____
 (3) _____ (7) _____
 (4) _____ (8) _____

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Date 15 / Jan / 2002 Name of Inventor(s) Maire Mahony
 Date _____ Name of Inventor(s) _____
 Date _____ Name of Inventor(s) _____
 Date _____ Name of Inventor(s) _____
 Date _____ Name of Inventor(s) _____